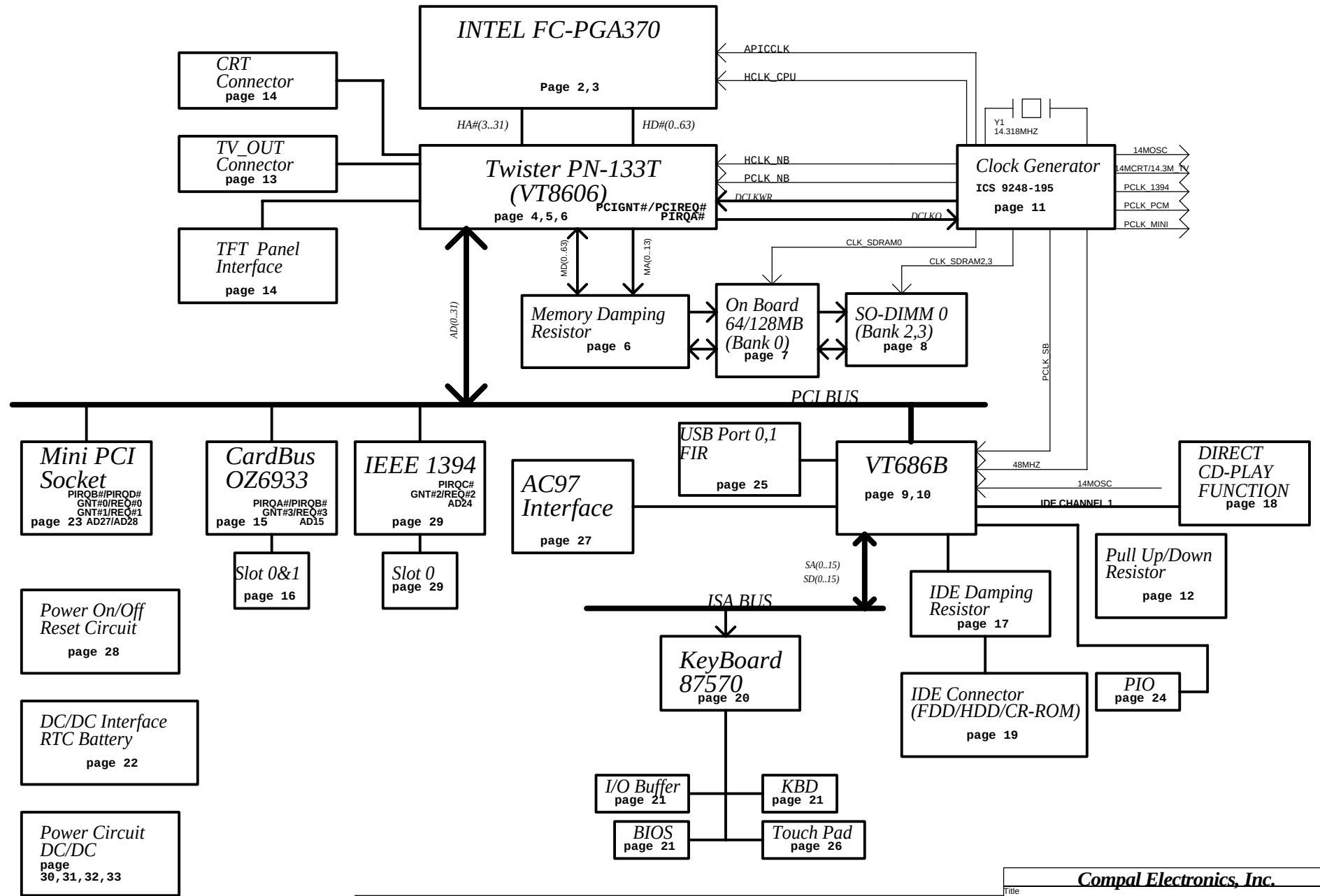
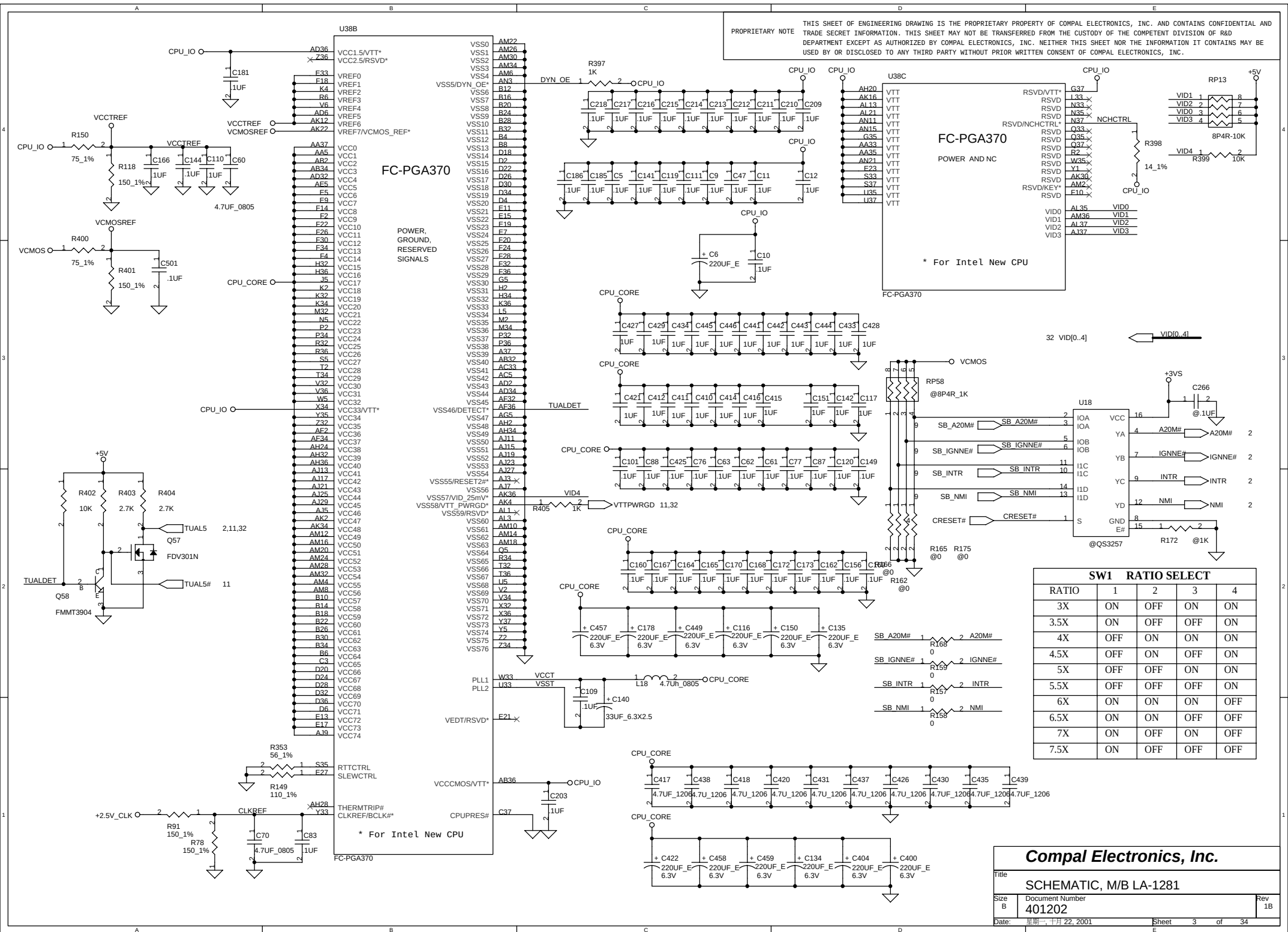
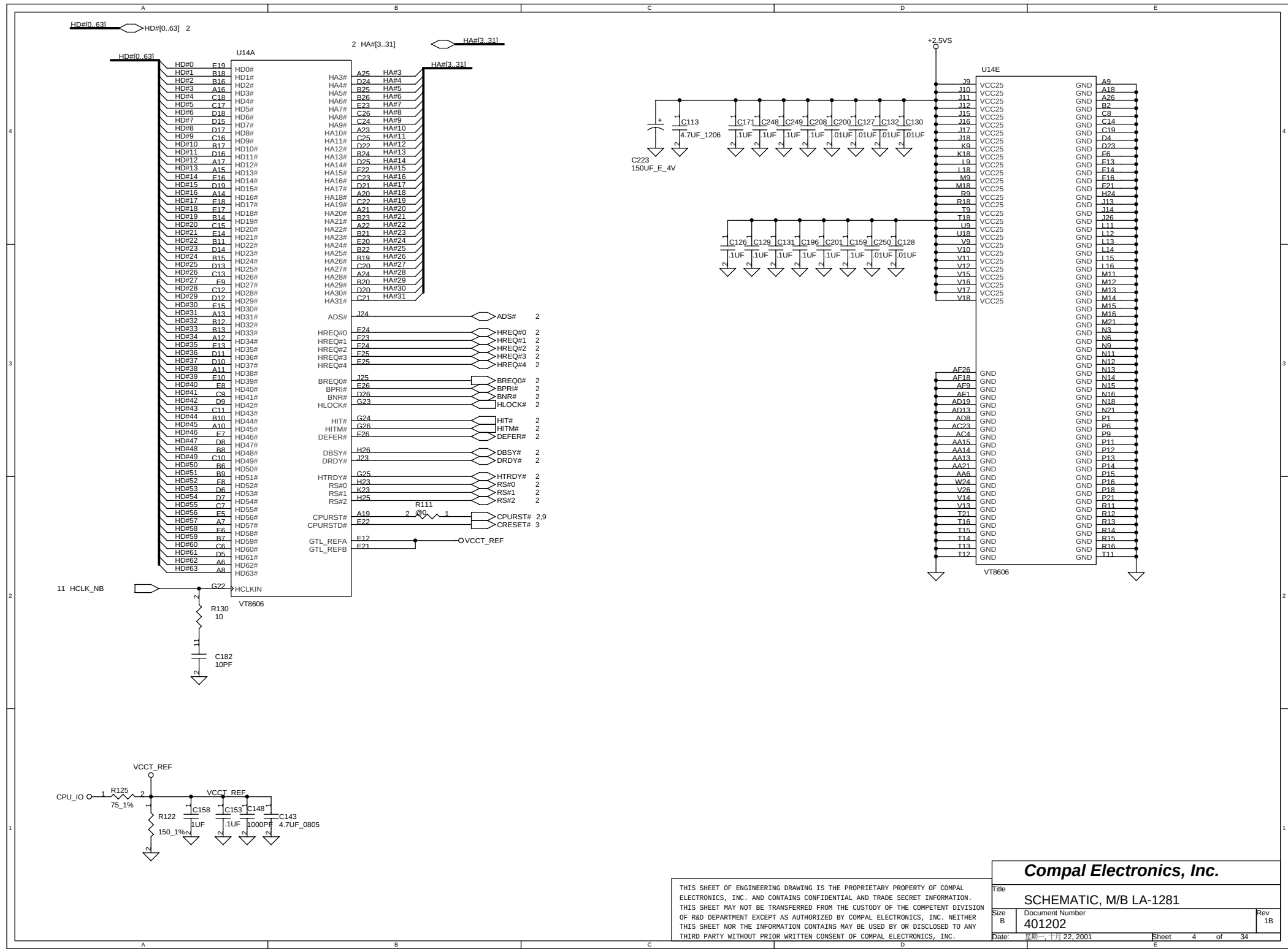


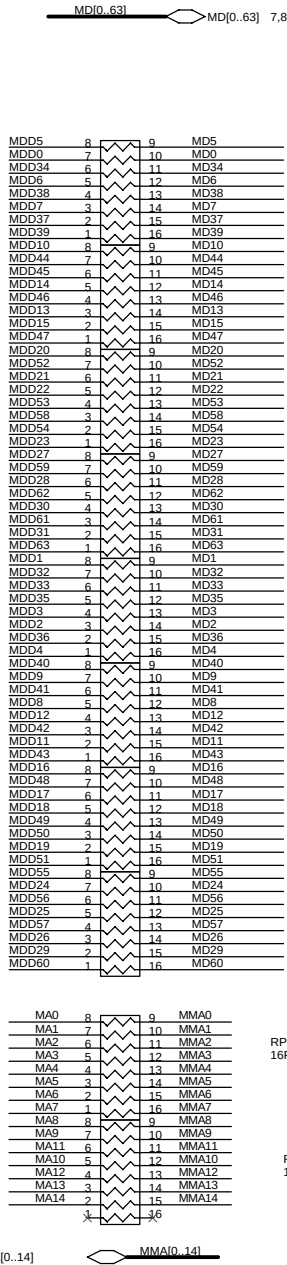
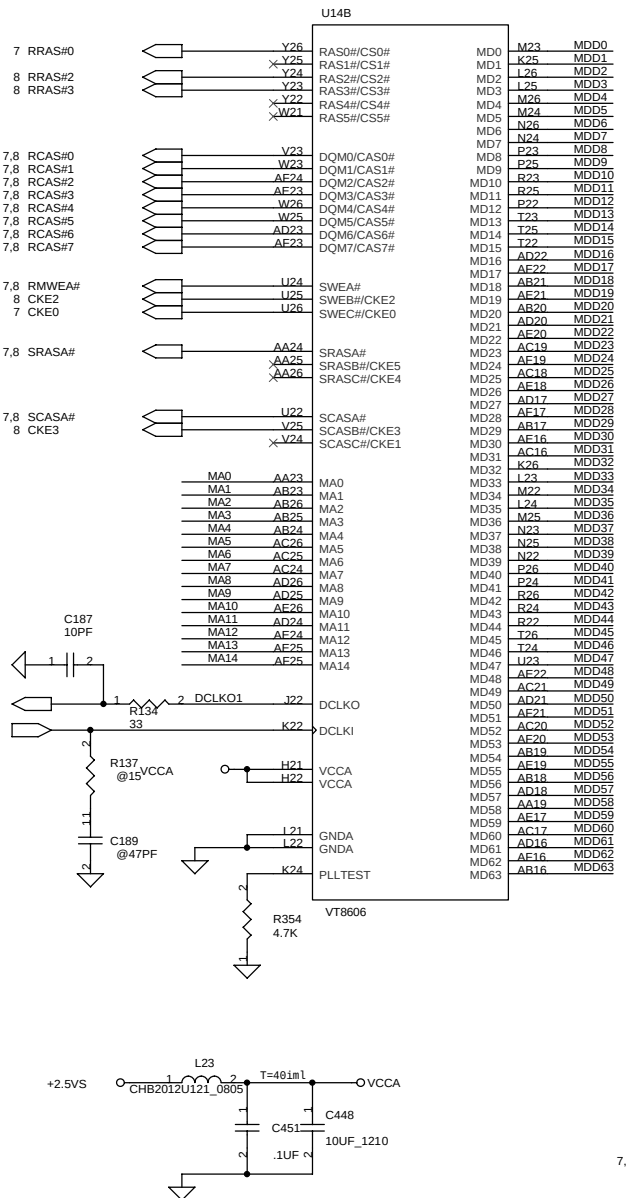






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RP34
16P8R-22

RP50
16P8R-22

RP54
16P8R-22

RP62
16P8R-22

RP38
16P8R-22

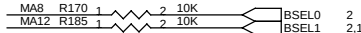
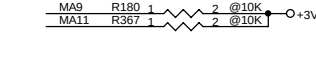
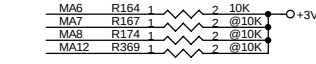
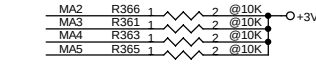
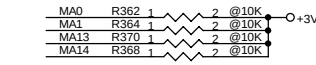
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16P8R-22

RP57
16P8R-22

RP61
16P8R-22

RP53
16P8R-22

RP56
16P8R-22



Strap	Description	Setting
MA12,8	CPU Clock Frequency	01=100Mhz 11=133Mhz 00=66Mhz
MA2	PCI Base Address Mapping	0=Map0 1=Map1
MA3	Graphic IO Enable/Disable	0=Enable 1=Disable
MA4	PCI Interrupt	0=Enable 1=Disable
MA7	Graphic Test Mode	0=Normal 1=Test
MA9	VGA Clock Select	0=PLL 1= External
MA11	IOQ Level	0=4Level 1=1Level
MA0,1,13,14	Panel Type	

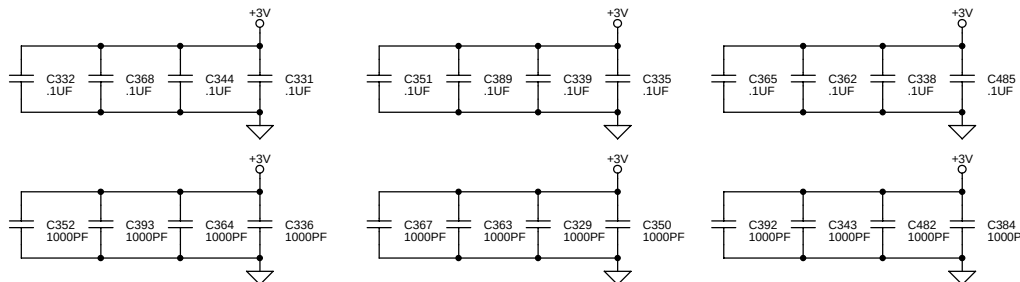
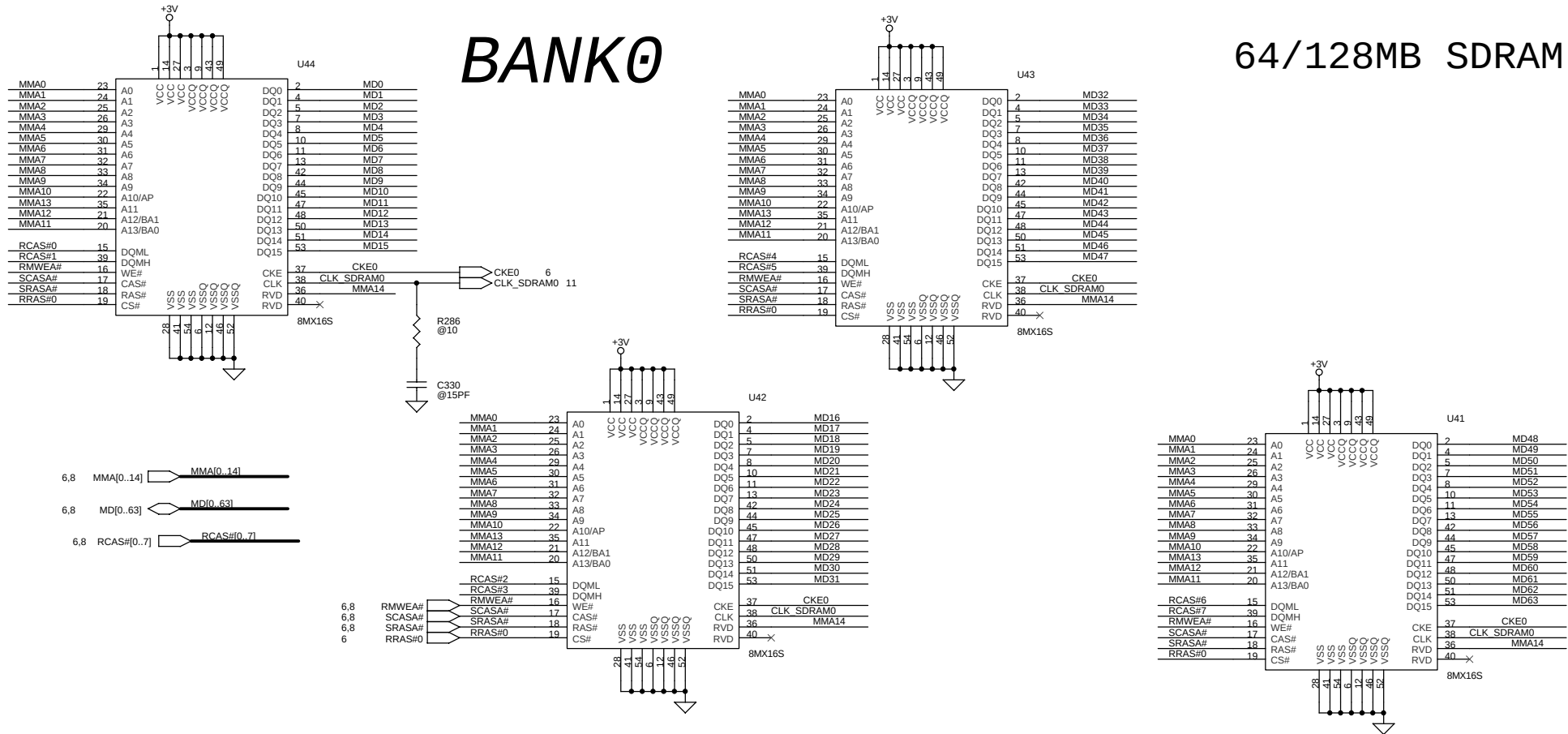
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BANK0

64/128MB SDRAM



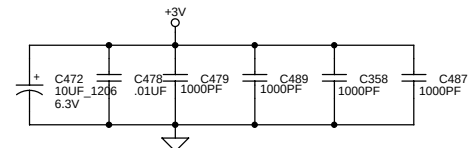
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BANK2/3

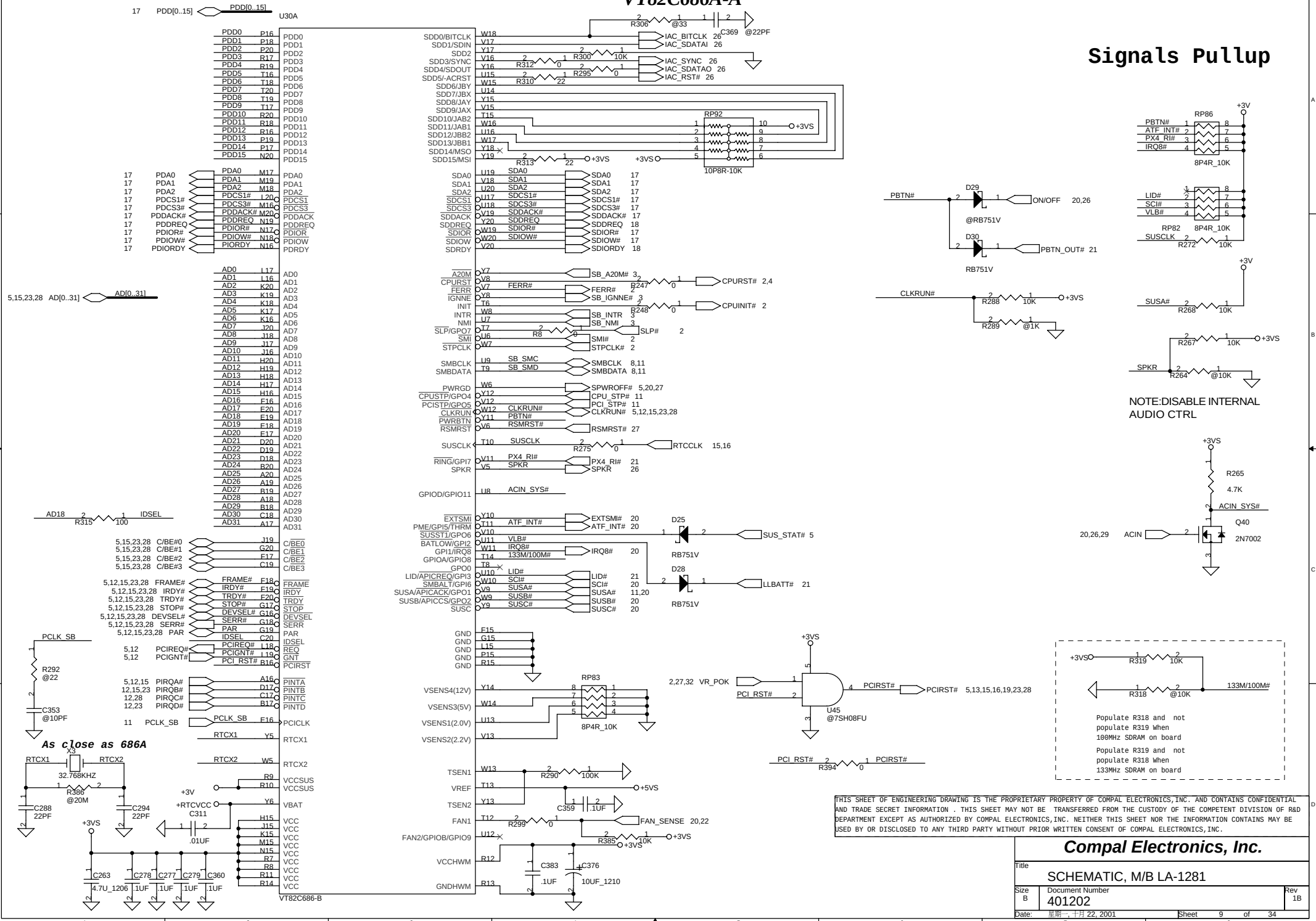


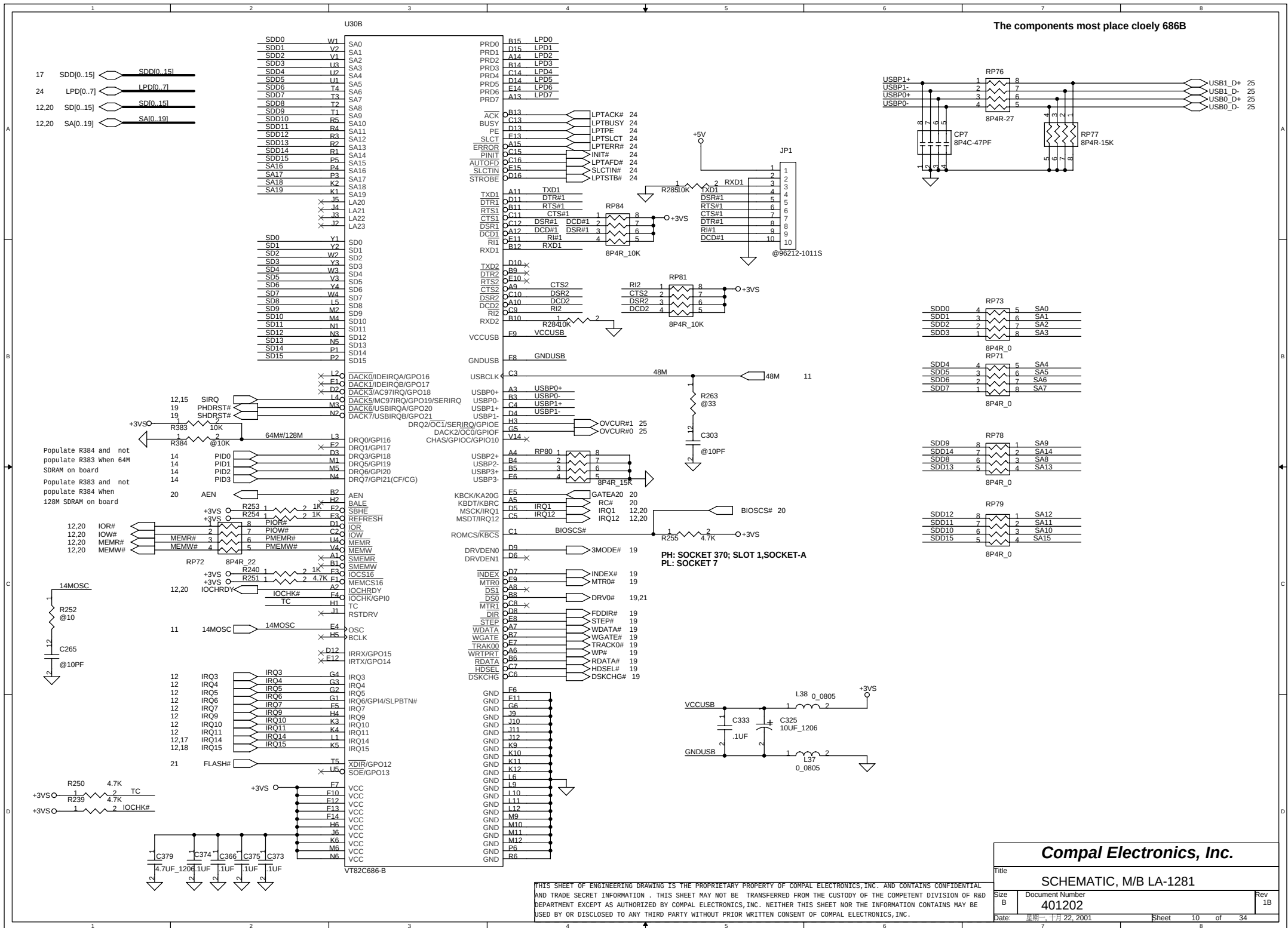
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VT82C686A-A

Signals Pullup





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Size

Document Number

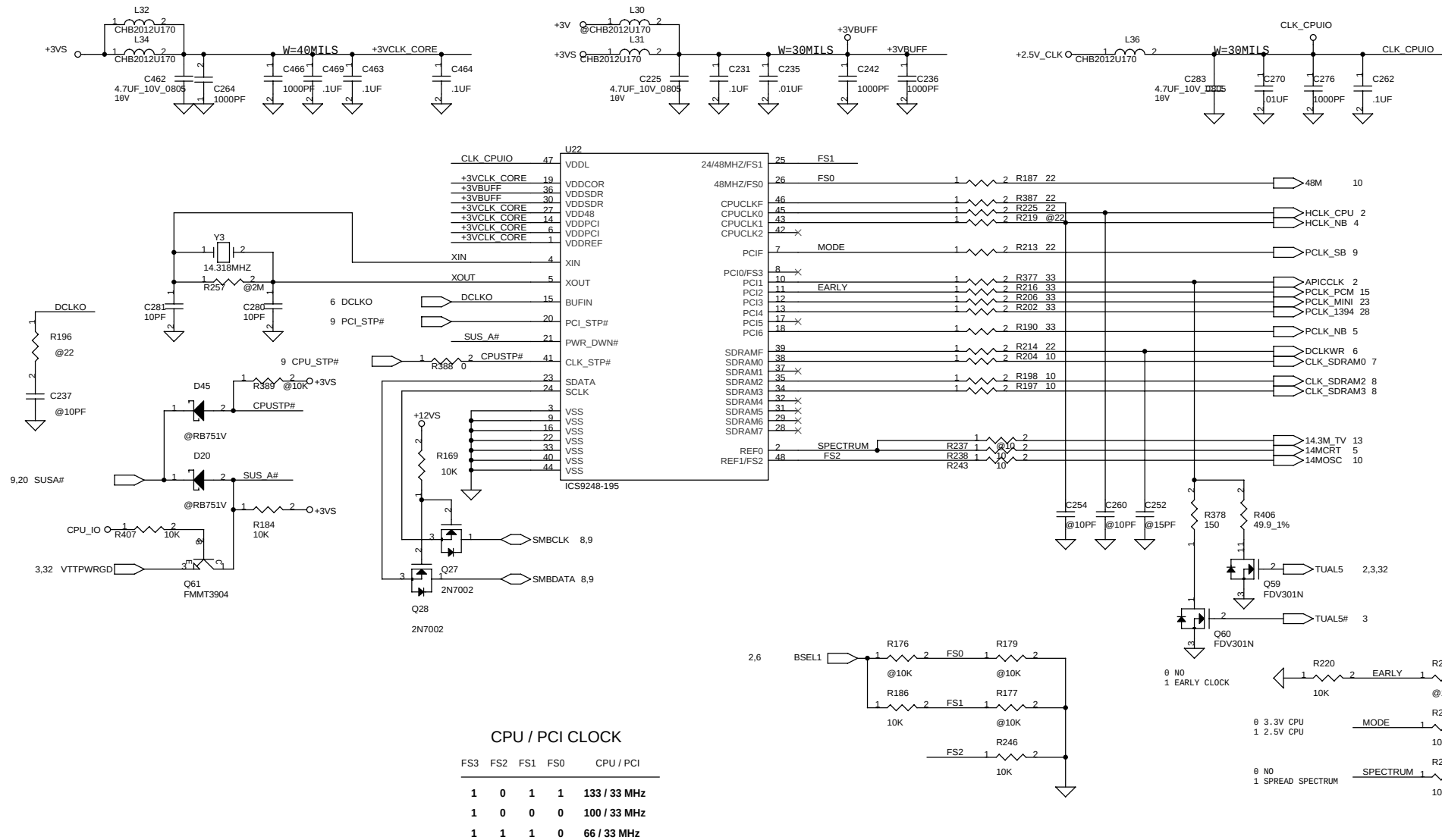
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CLOCK GENERATOR & BUFFER



CPU / PCI CLOCK				
FS3	FS2	FS1	FS0	CPU / PCI
1	0	1	1	133 / 33 MHz
1	0	0	0	100 / 33 MHz
1	1	1	0	66 / 33 MHz

Compal Electronics, Inc.

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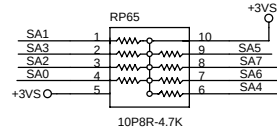
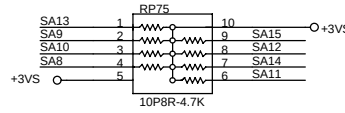
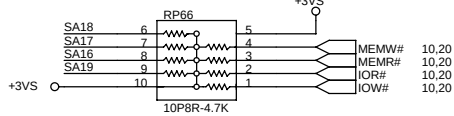
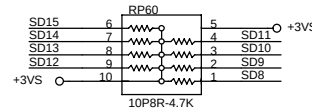
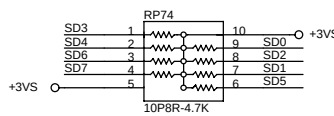
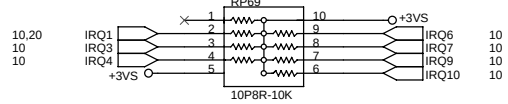
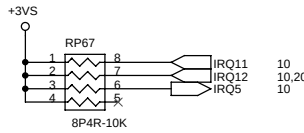
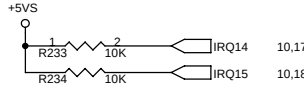
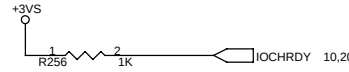
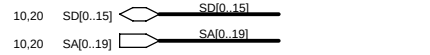
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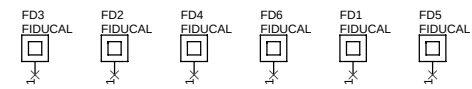
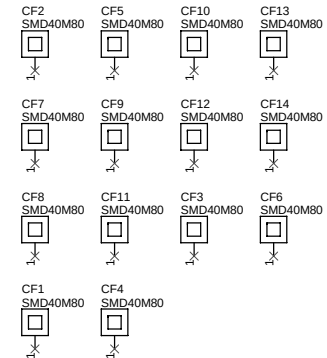
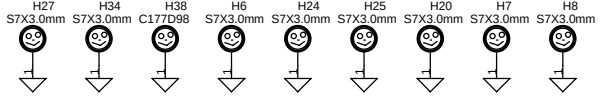
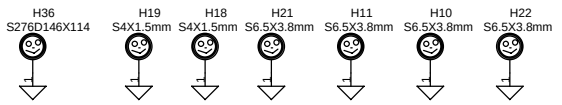
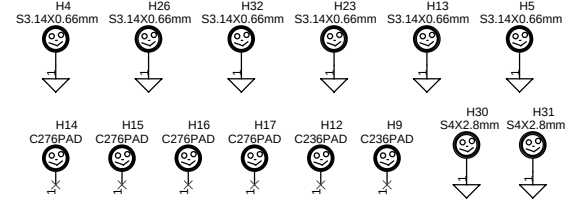
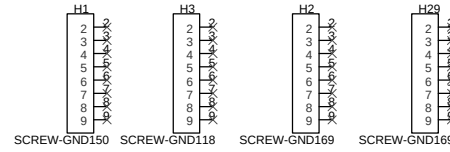
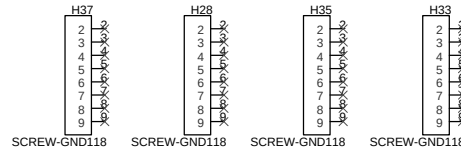
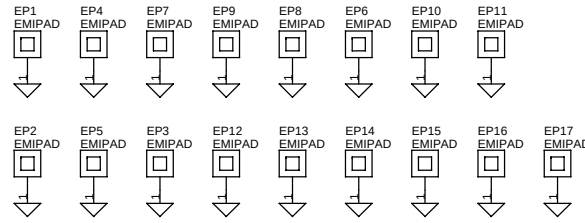
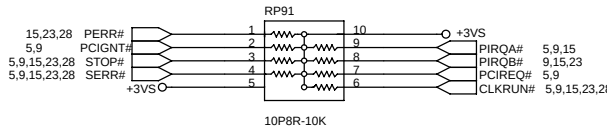
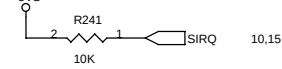
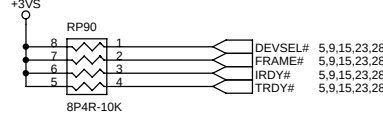
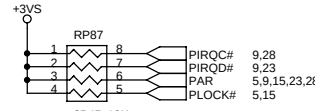
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ISA BUS Pullup



PCI BUS Pullup



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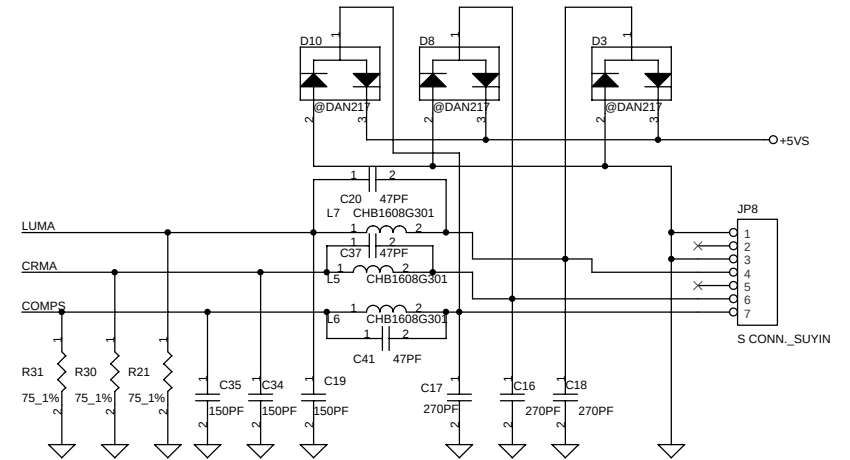
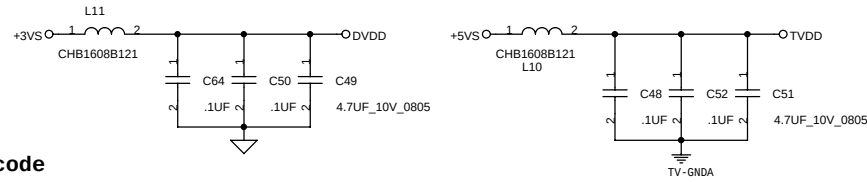
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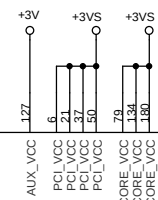
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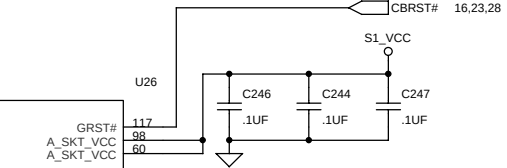
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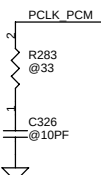


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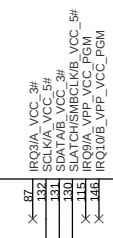


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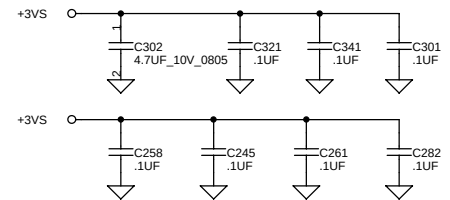
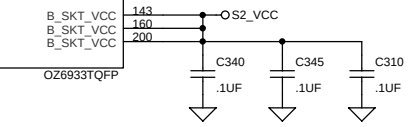
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11 PCLK_PCM
5,9,12,23,28 DEVSSEL#
5,9,12,23,28 FRAME#
5,9,12,23,28 IRDY#
5,9,12,23,28 TRDY#
5,9,12,23,28 STOP#
5,9,12,23,28 PAR#
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5,9,12,23,28 SERR#
5 REQ#3
5 GNT#3
5,9,12 PIRQA#
9,12,23 PIRQB#
5,12 PLOCK#
5,9,13,16,19,23,28 PCIRST#
21 PCM_PME#
5,9,12,23,28 CLKRUN#
23 PCM_R#
26 PCM_SPK#
21 PCM1_LED#
21 PCM2_LED#
10,12 SIRQ



13 C/BE#3
25 C/BE#2
36 C/BE#1
47 C/BE#0
15 IDESEL
1 PCI_CLK
27 DEVSSEL#
31 FRAME#
29 IRDY#
30 TRDY#
32 STOP#
35 PAR#
33 PERR#
34 SERR#
3 PCI_REQ#
2 PCI_GNT#
203 IRQ9/INTA#
204 IRQ4/INTB#(A_VPP_PGM)
58 LOCK#
207 RST#
163 IRQ12/PME#
208 IRQ14/CLKRUN#
72 IRQ15/RI_OUT#
128 SPKR_OUT#
133 LEDO#/SKTA_ACTV
193 IRQ11/SKTB_ACTV
205 IRQ5/SERIRQ#
206 IRQ7/SIN#/B_VPP_PGM



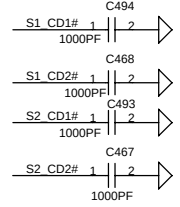
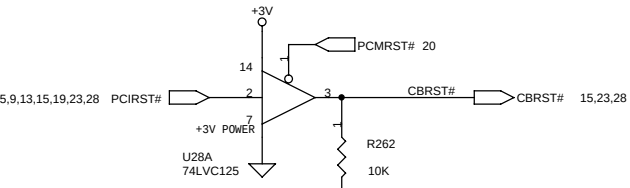
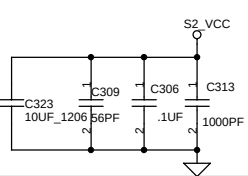
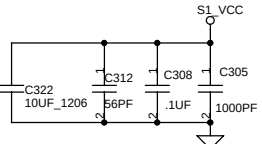
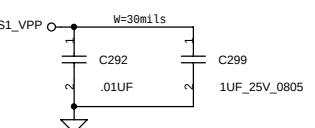
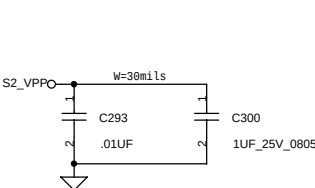
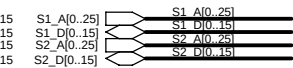
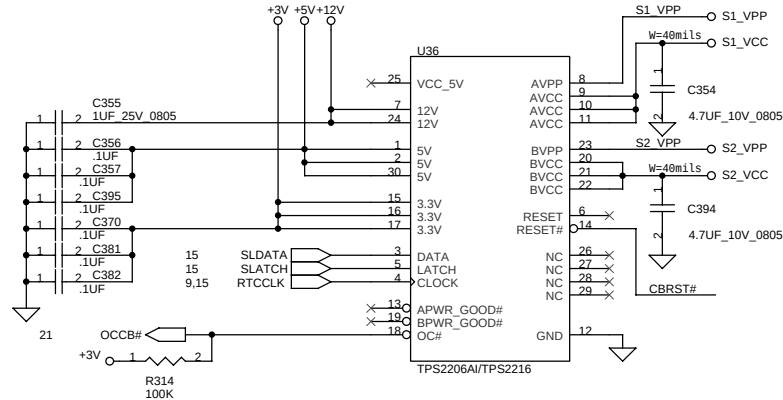
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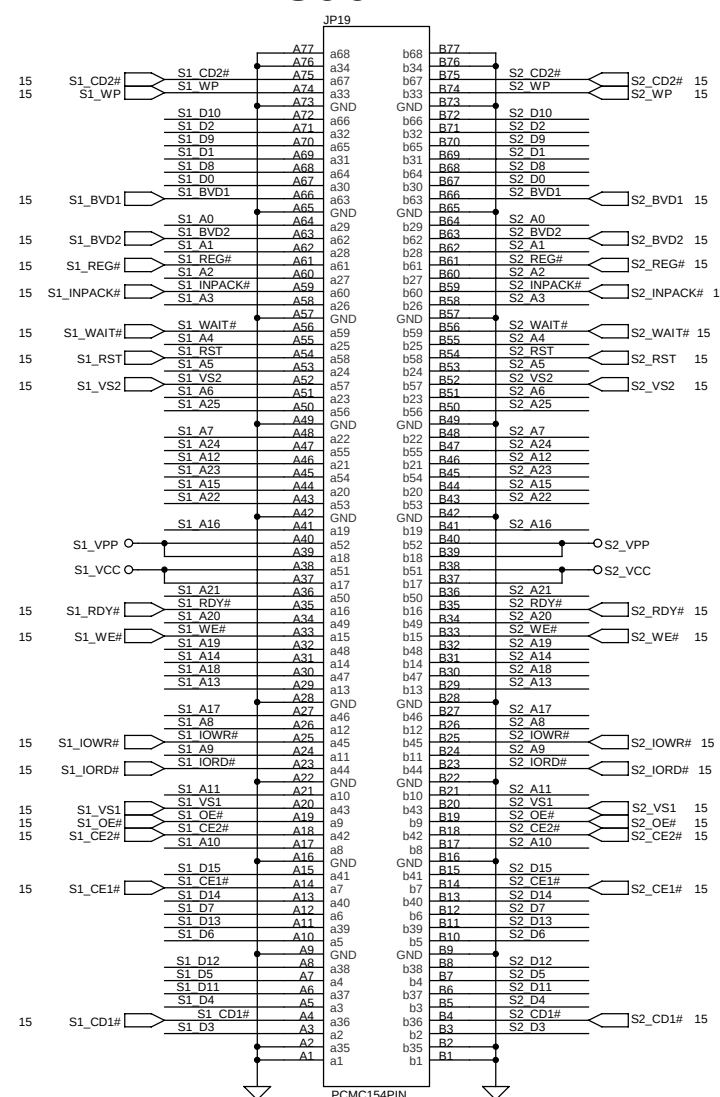
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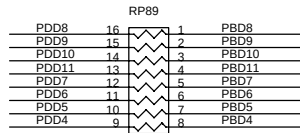
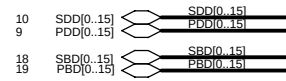
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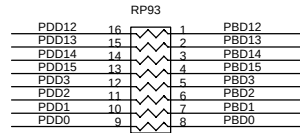
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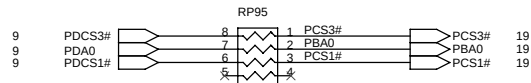
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Place them close SB



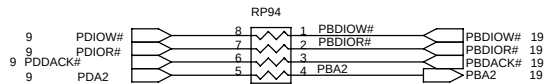
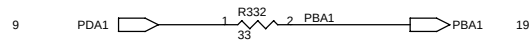
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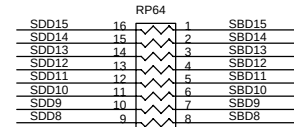
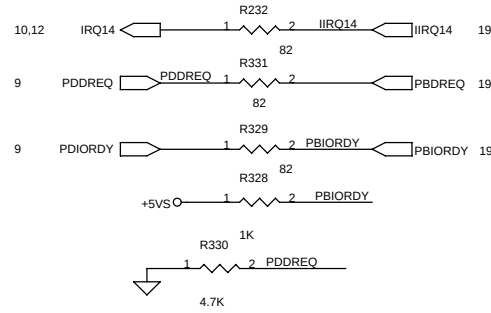
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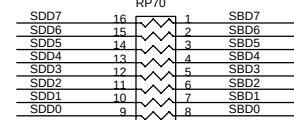
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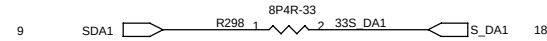
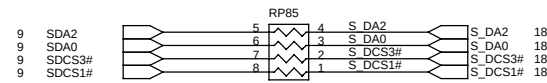
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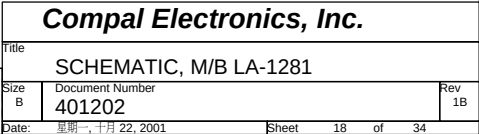


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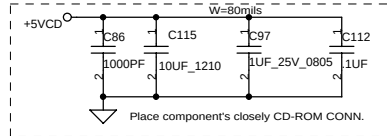
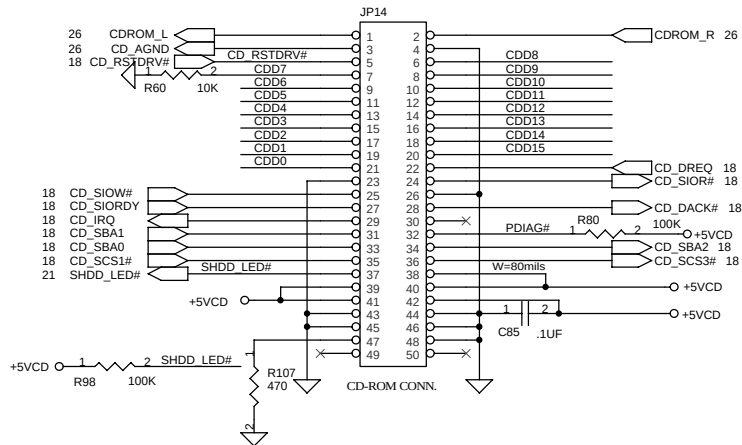
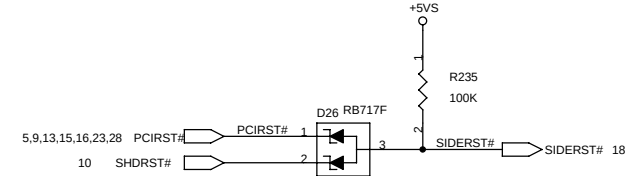
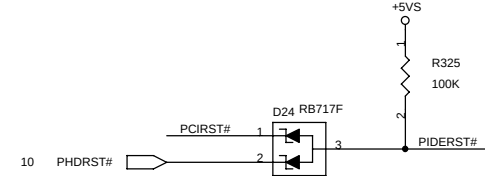
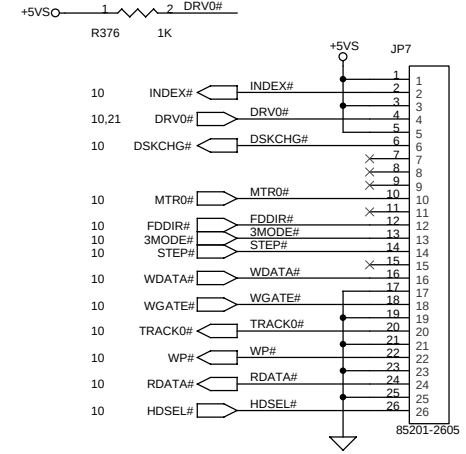
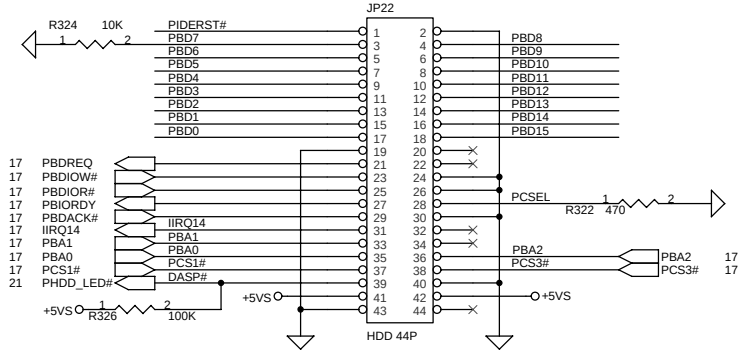
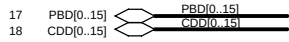
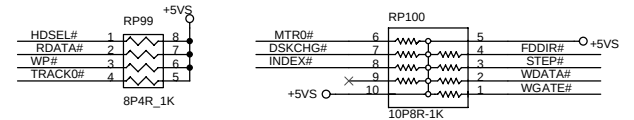
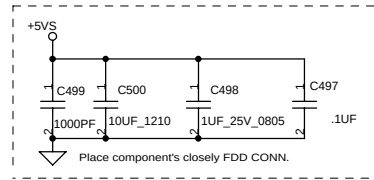
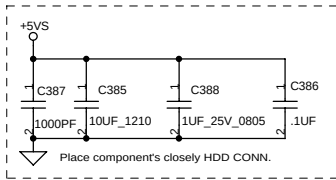
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IDE,CD-ROM & FDD Module CONN.

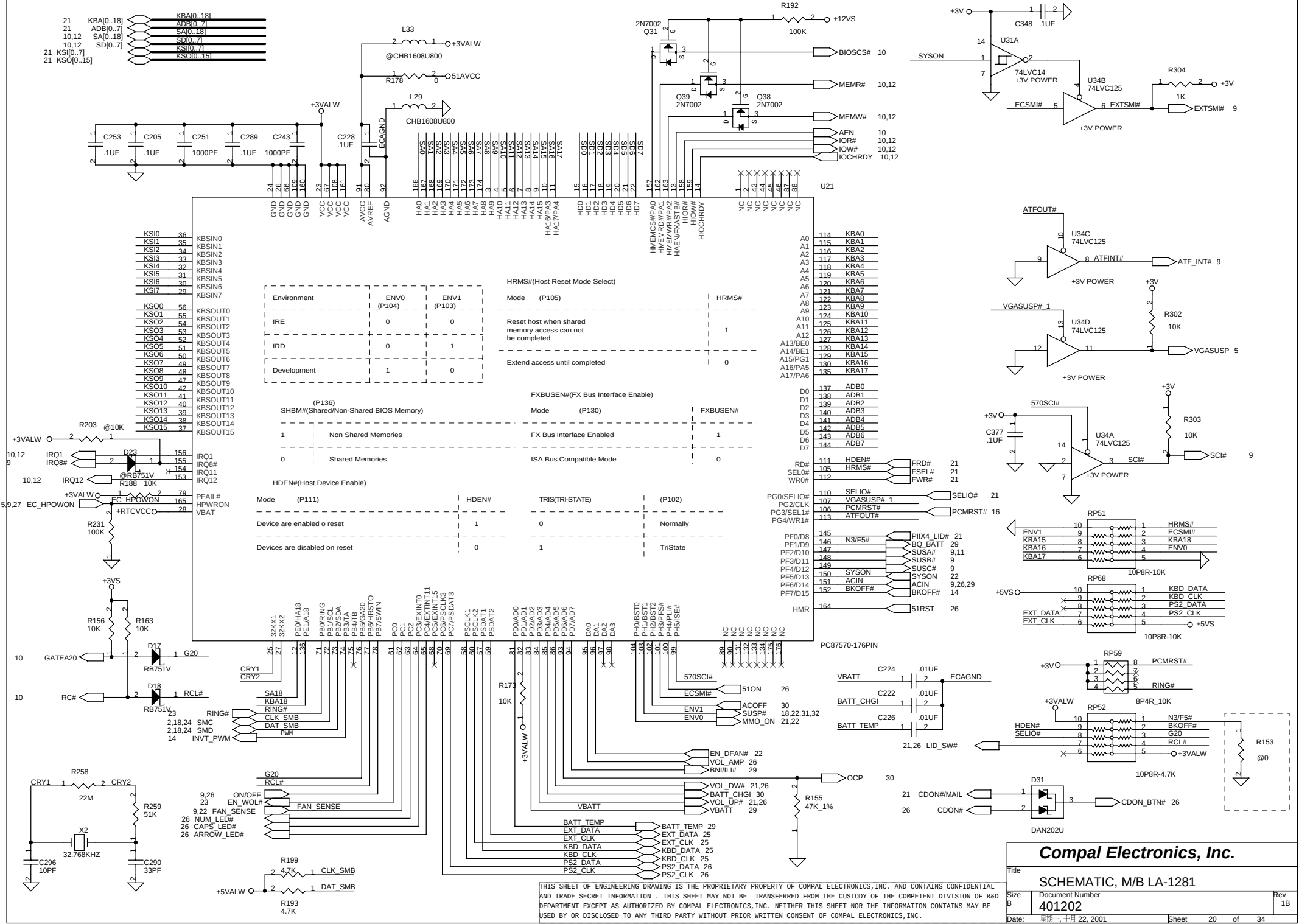


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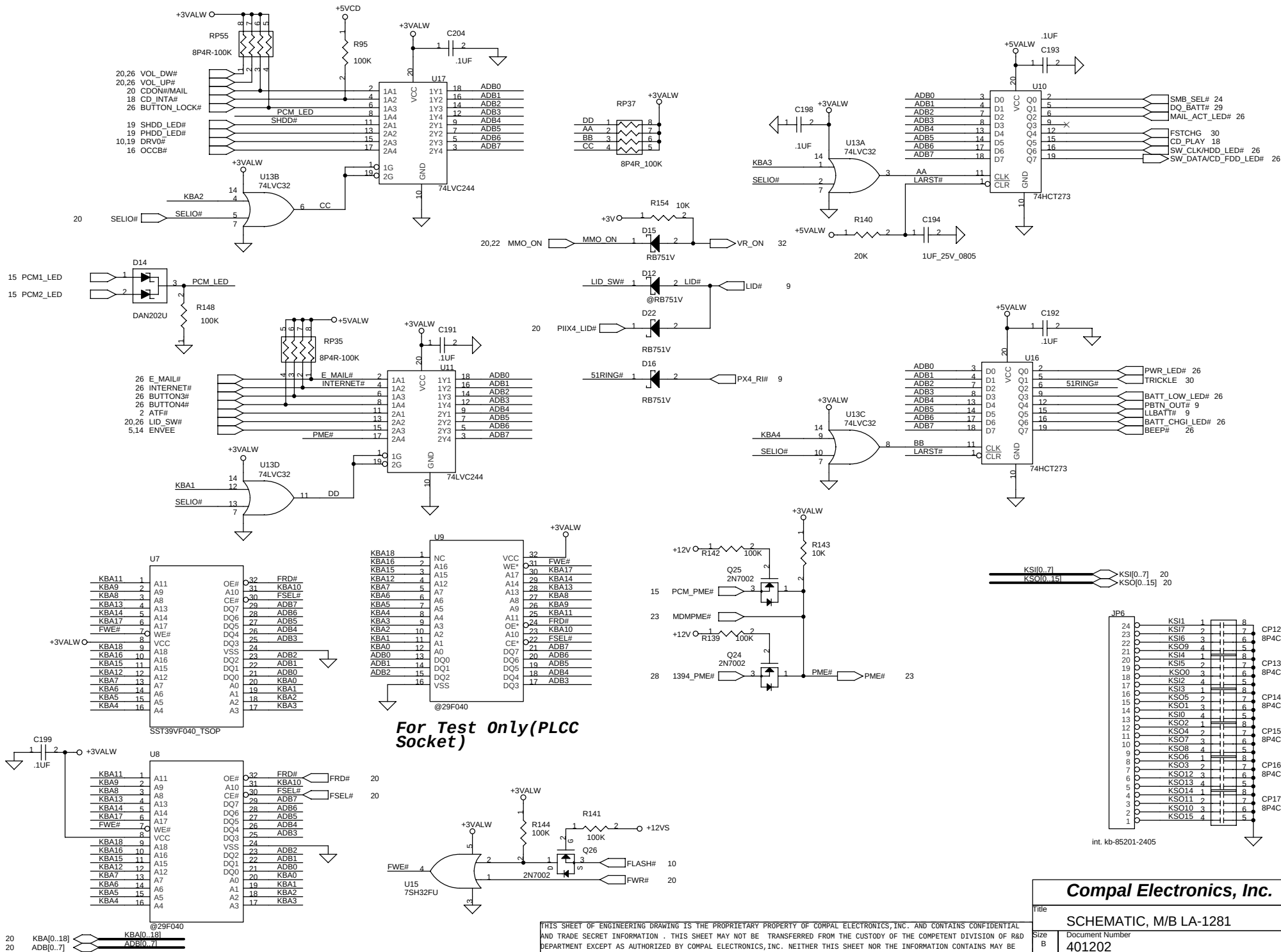
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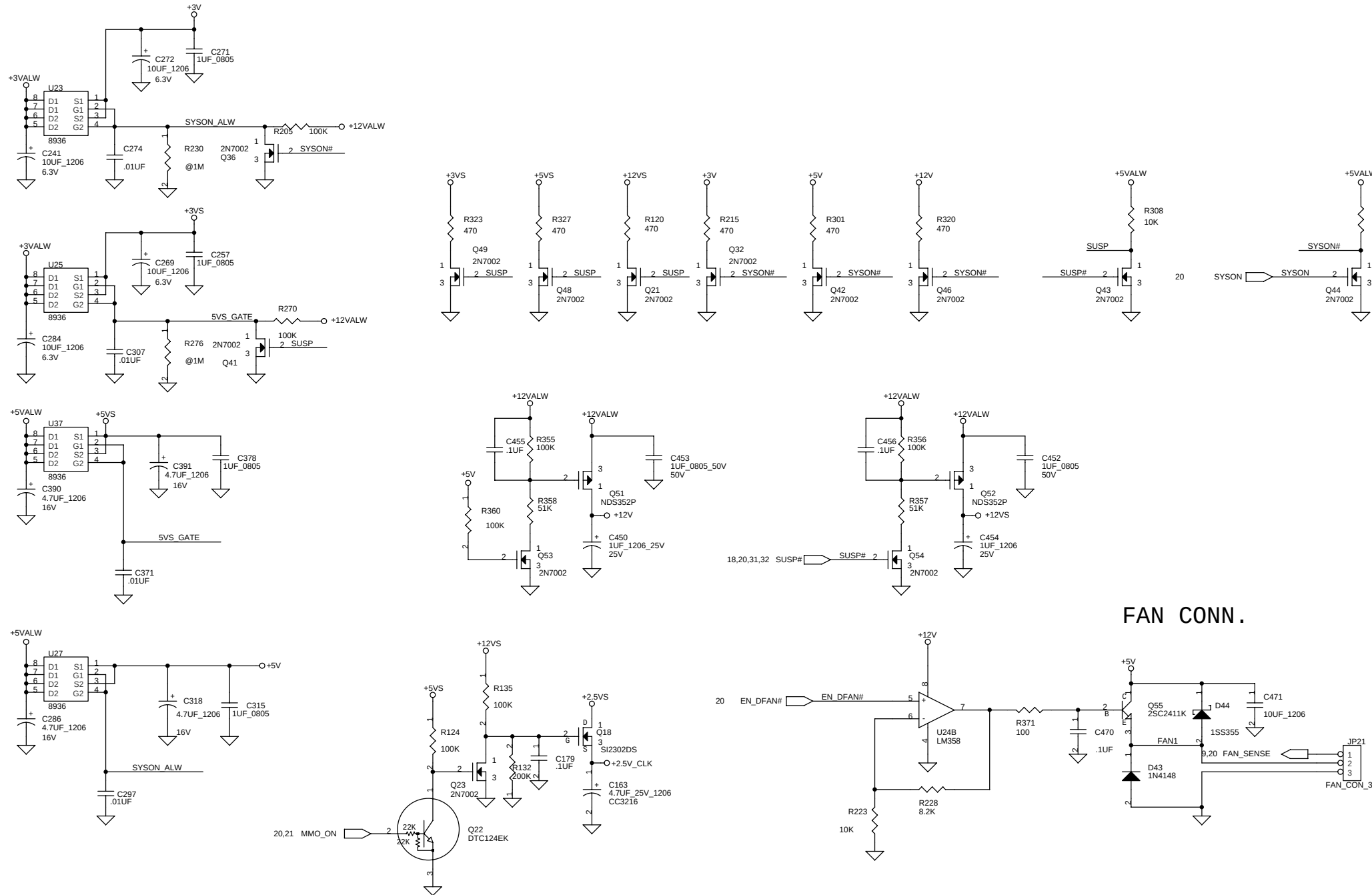
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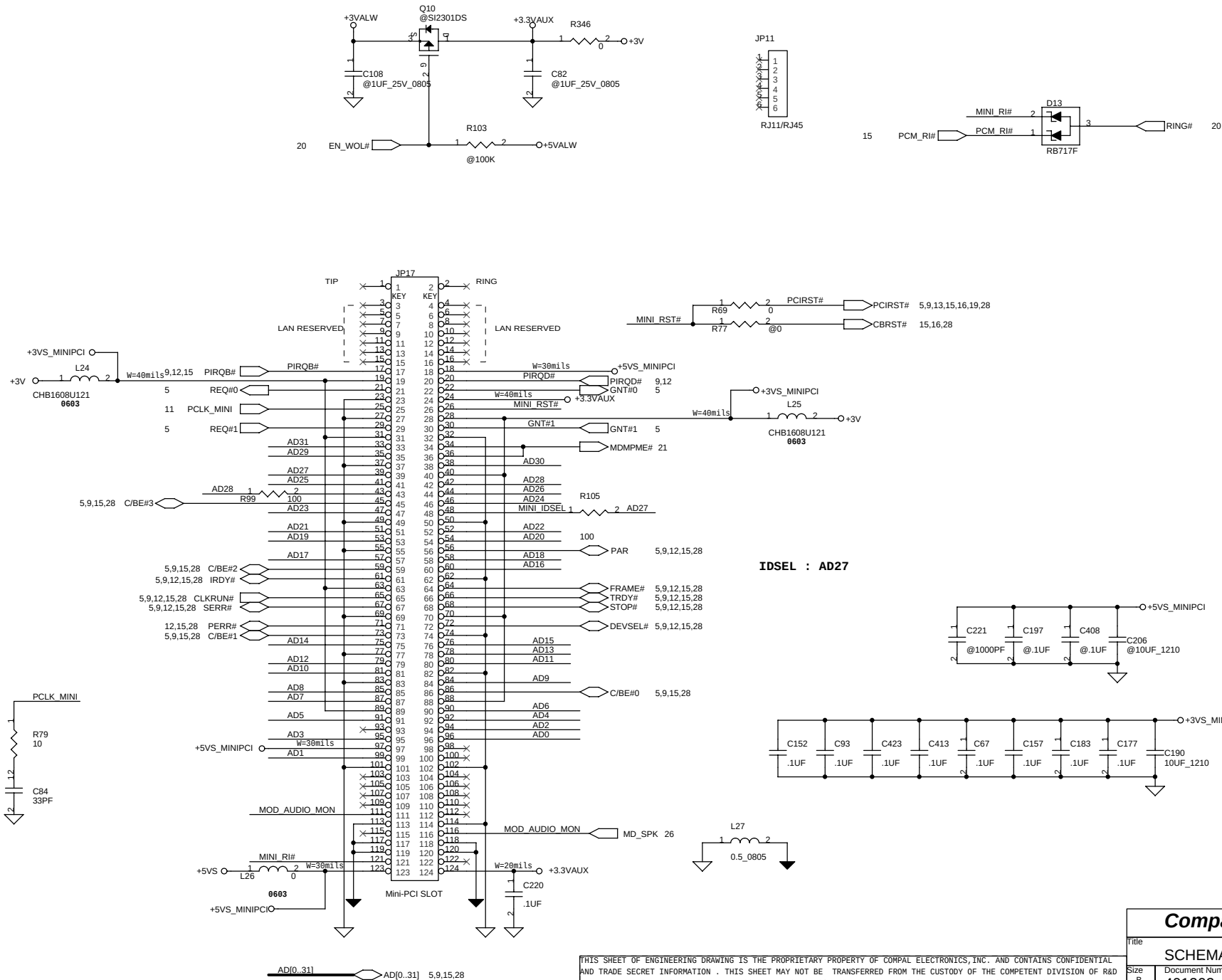
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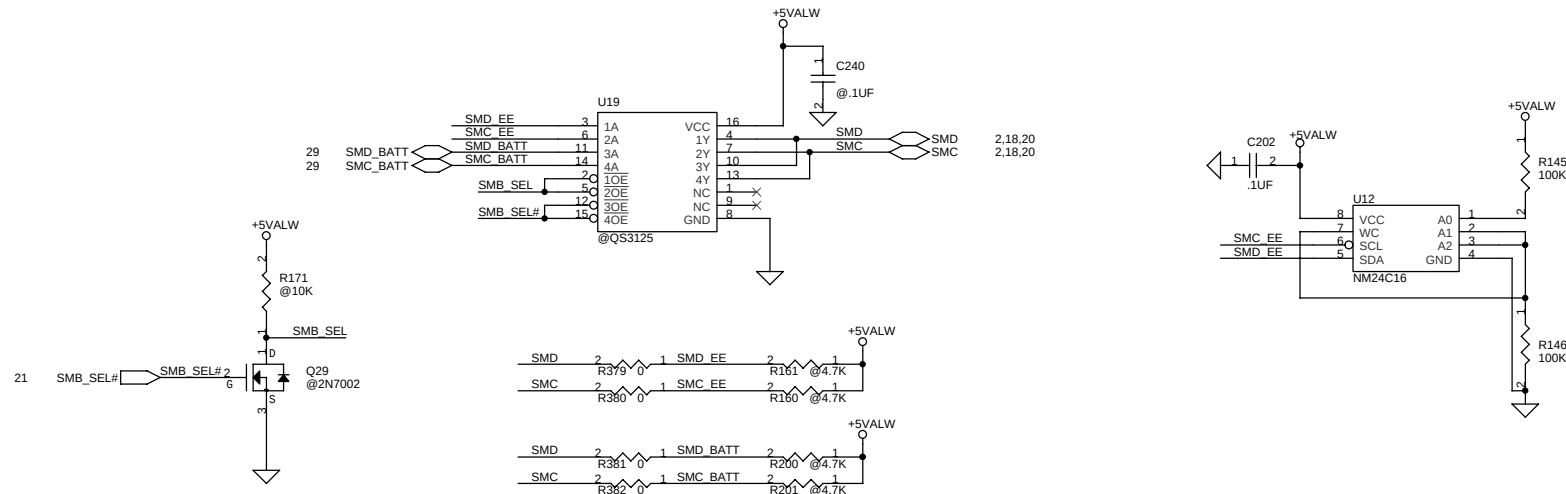
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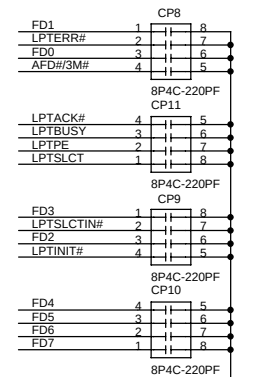
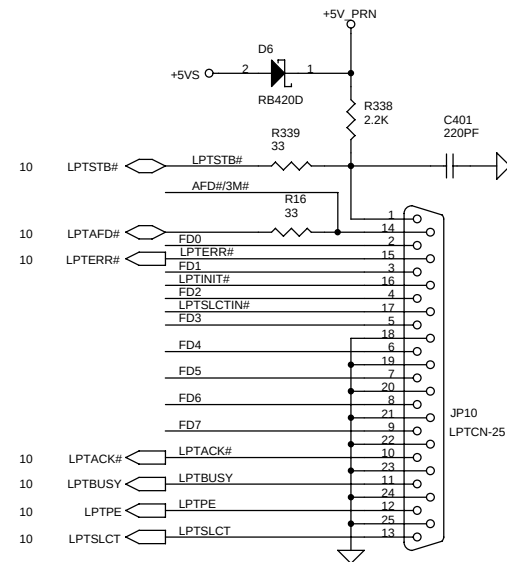
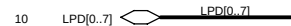
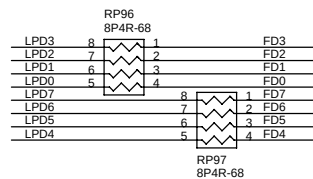
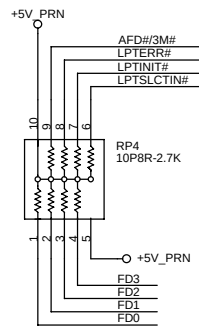
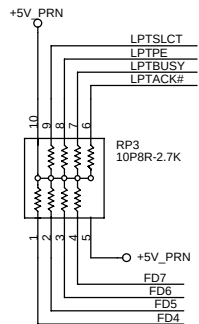
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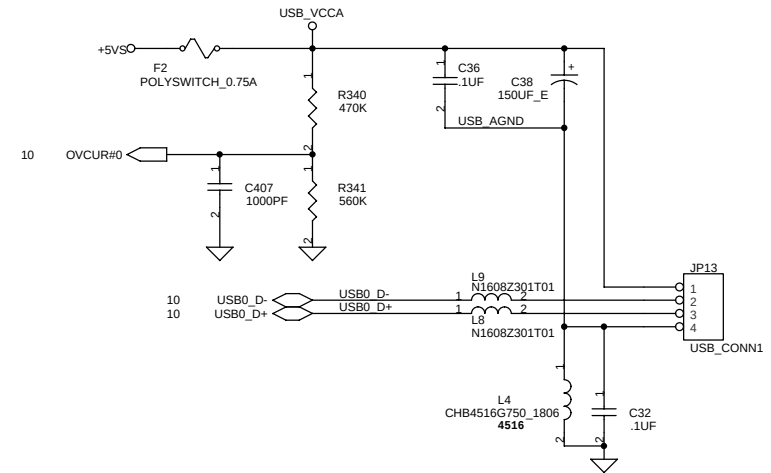
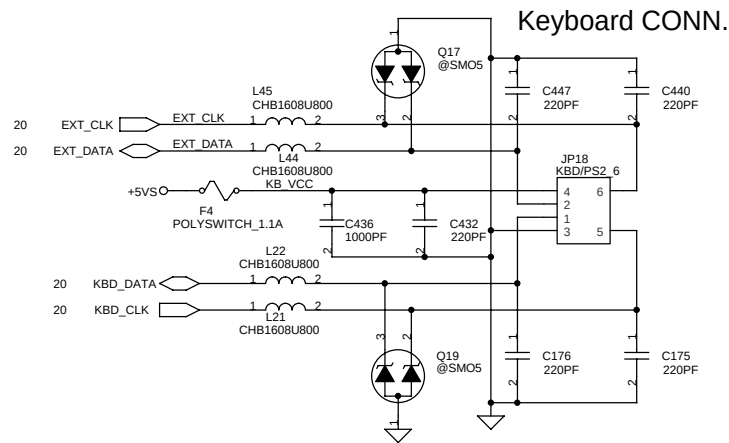
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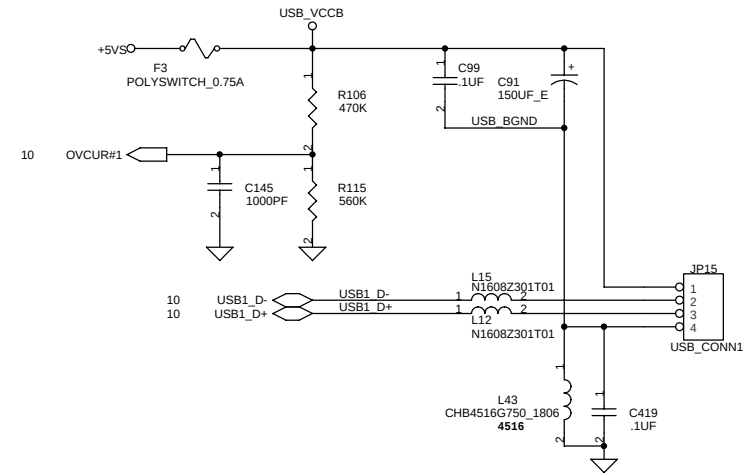
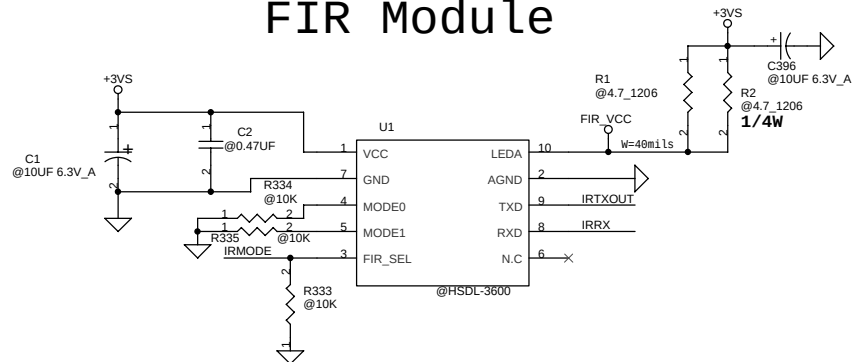
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FIR Module

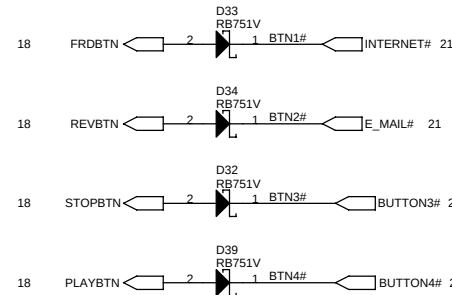
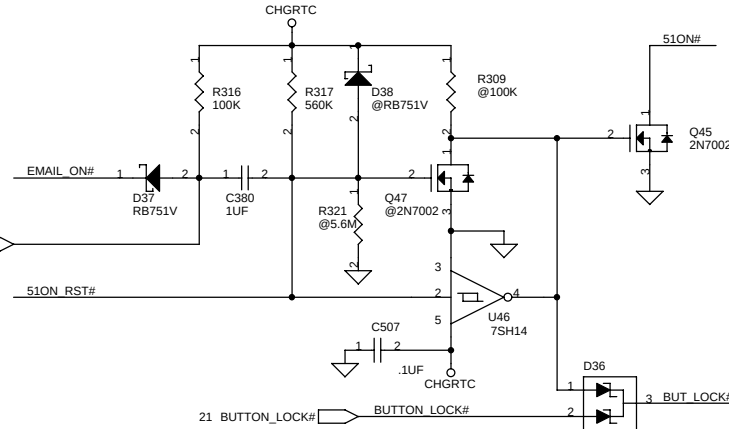
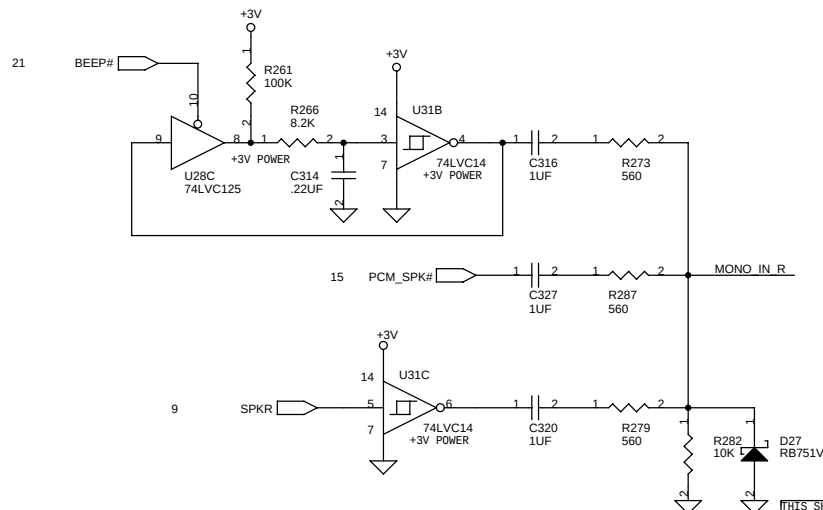
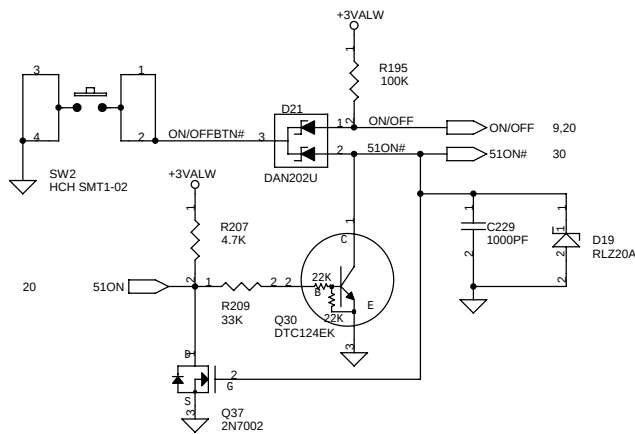
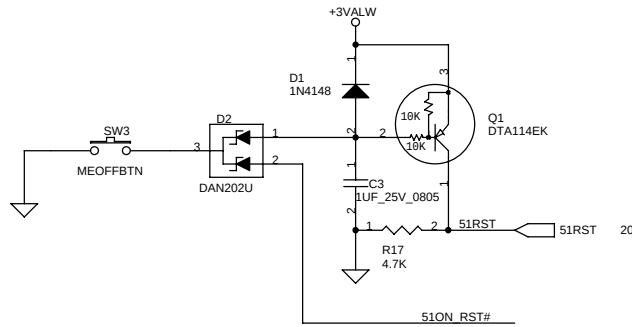


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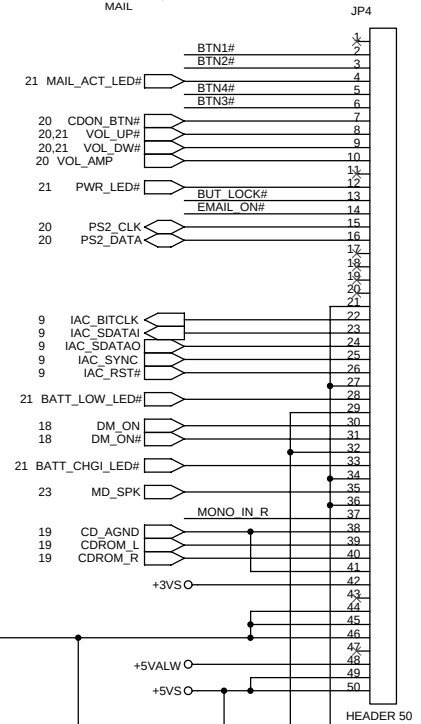
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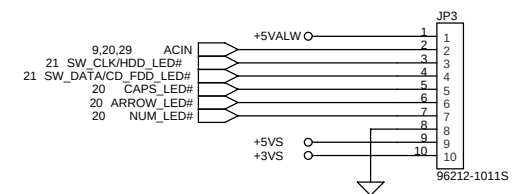
Reset Button



CDON_BTN# is Low, enable CD_PLAY
MAIL is High, enable
MAIL



System Window Connector

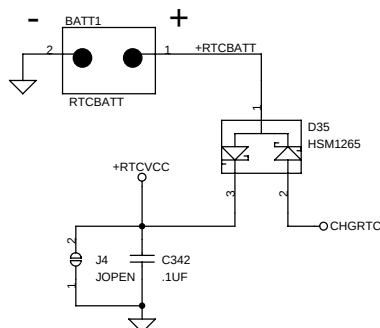


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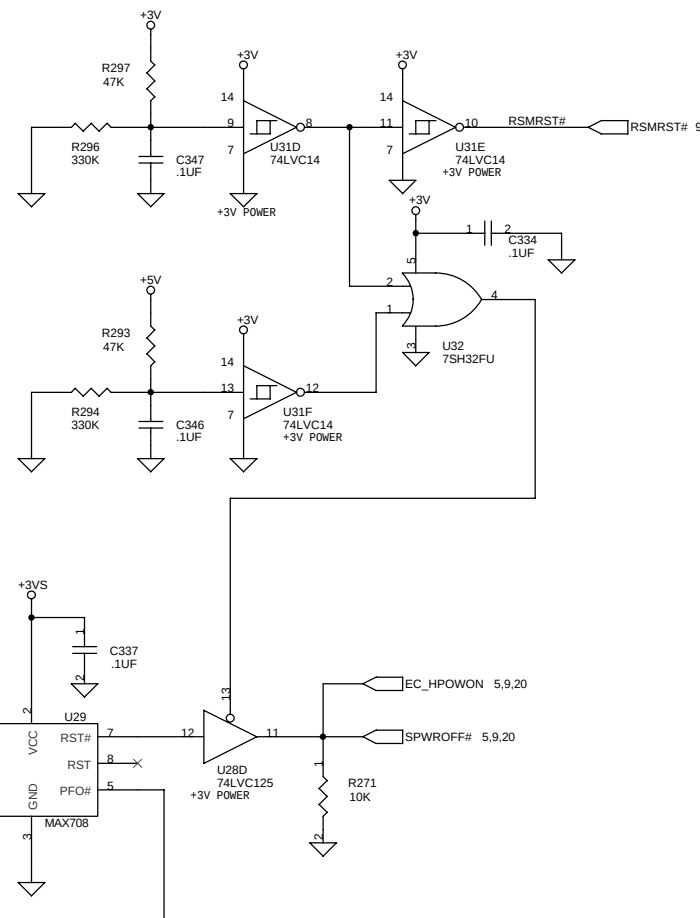
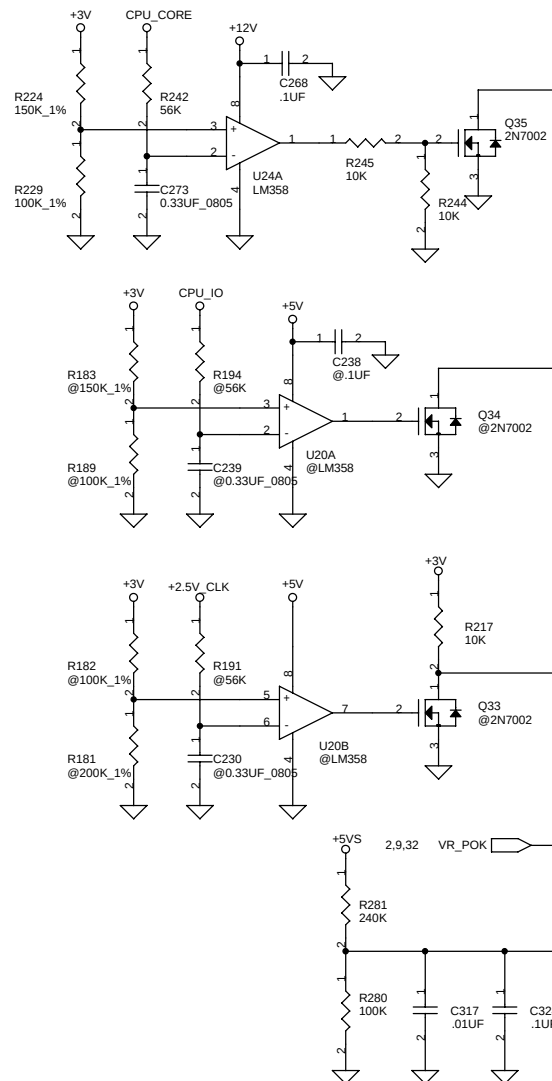
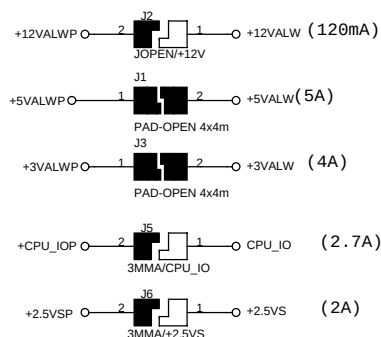
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RTC BATT



DC/DC INTERFACE



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ACIN: CC:2.87A/ OCP:3.48A

Charger : CC:2.2A CV:16.8V for LI-ION/CV:14.4V for NI-MH

Charger : CC:2.2A CV:14.4V

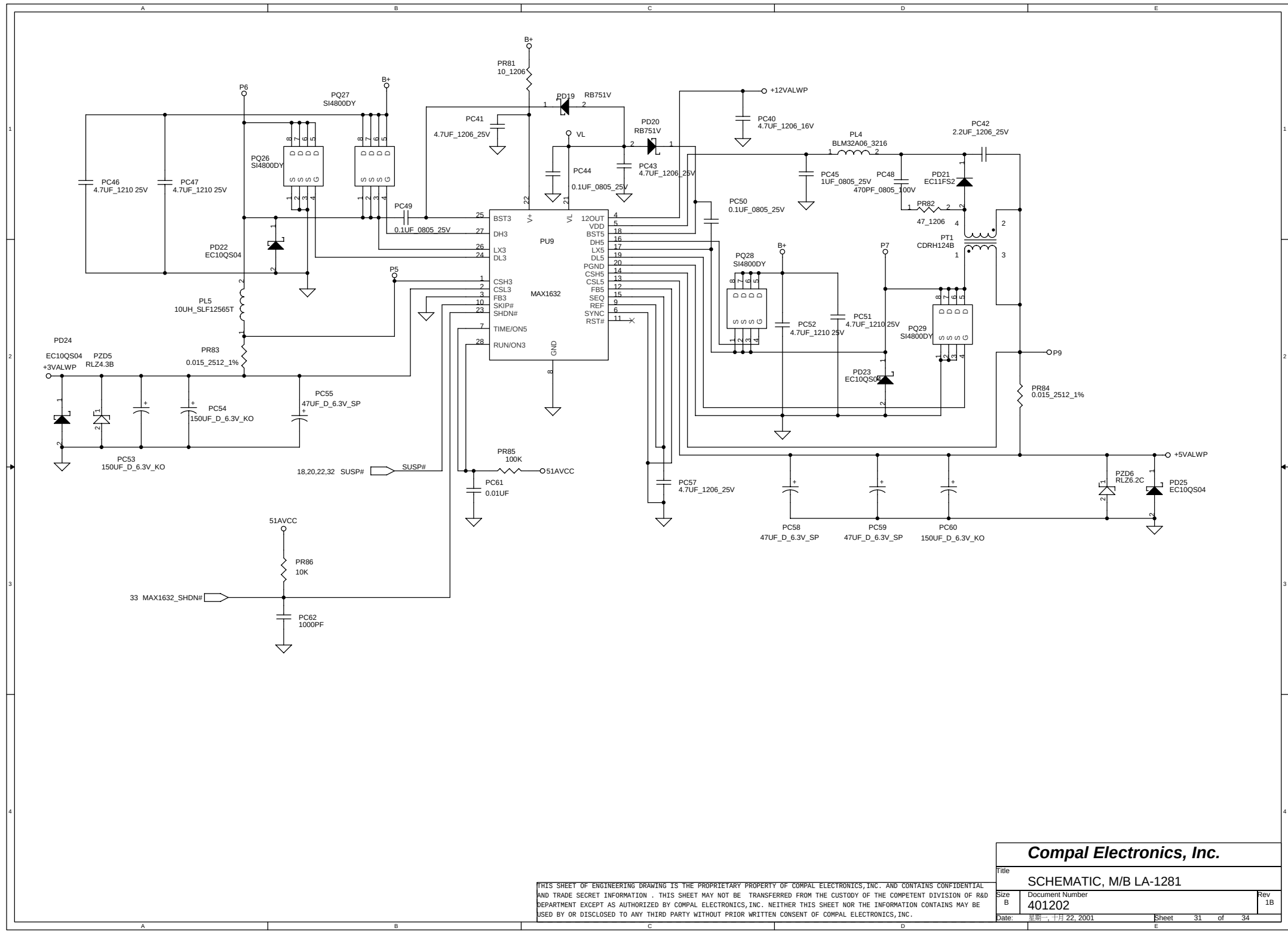
RTC: 3.5V

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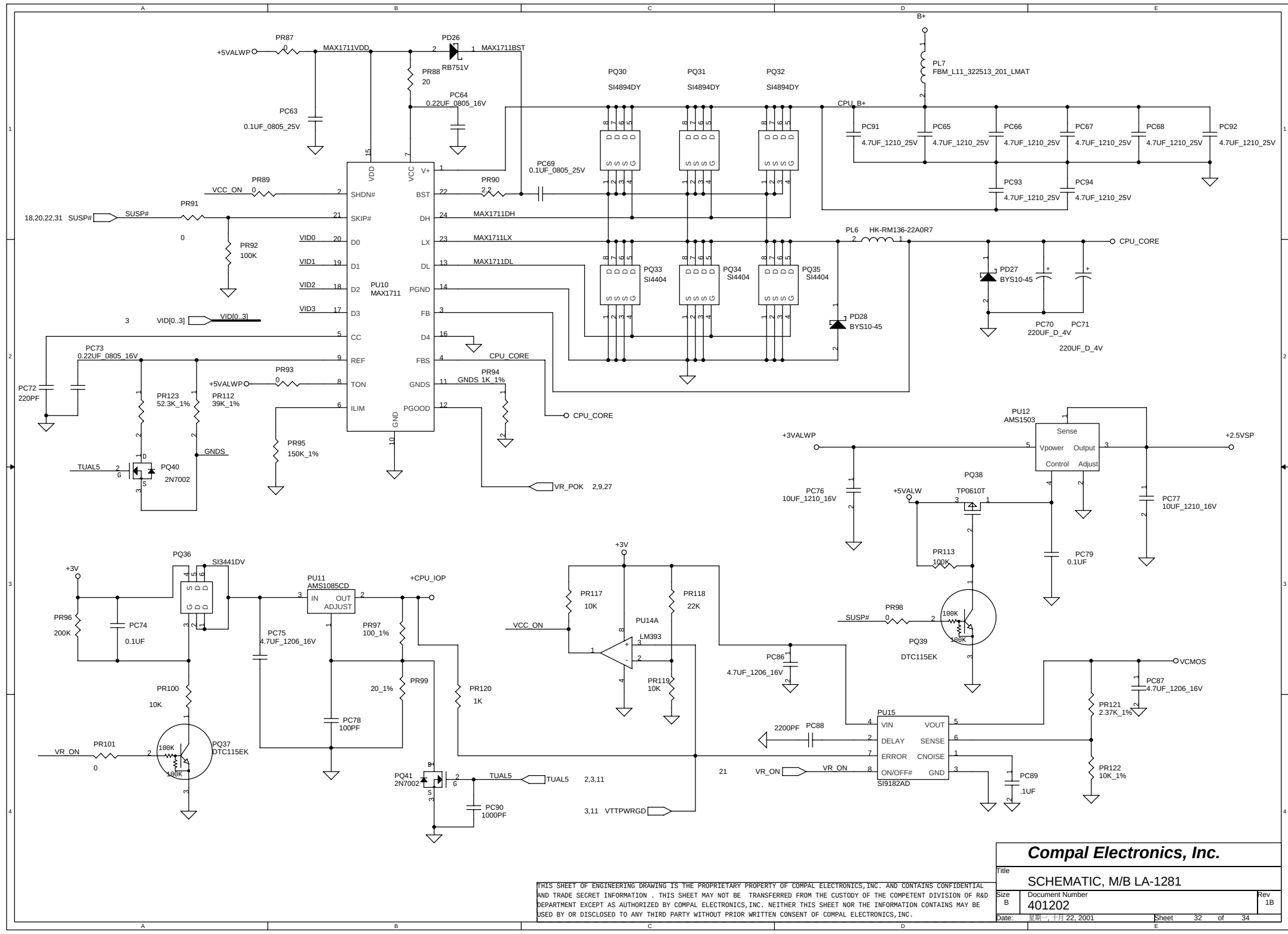
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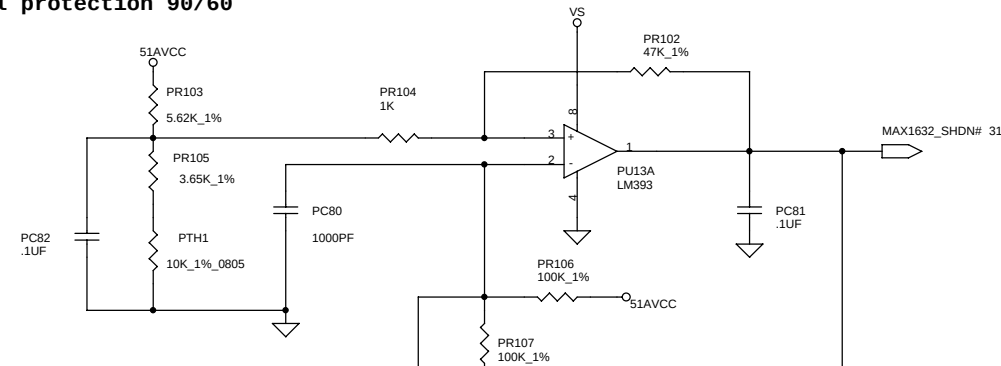
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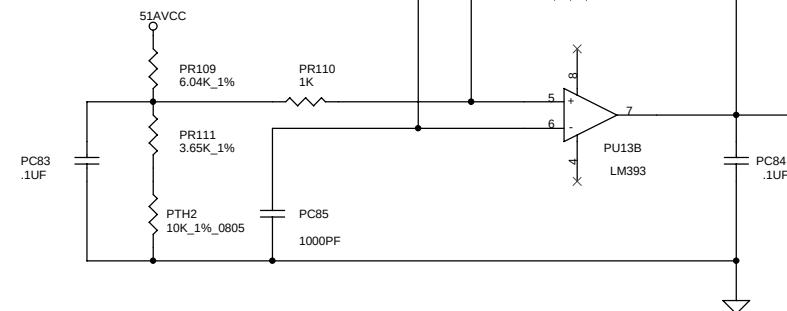
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For CPU thermal protection 90/60
度C



For Battery thermal protection 83/54
度C



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CY23 PIR LIST

08/28/01 Written by Jerry

P11: Add Q61 and R407 for VTTPWRGD

***** Rev1.0 PIR List *****

09/10/01 Written by Jerry

P26: Add @ at R309 and Q47

 Add C507 and U46

 Change value of R317 to 510K

P27: Remove @ at R224, R229, R242, R245, R244, C273, and Q35

P32: Add PC91, PC92, PC93, and PC94.

09/12/01 Written by Jerry

P06: Change value of RP34, RP50, RP54, RP62, RP38, RP33, RP57,

RP61, RP53, and RP56 to 16P8R-22

P26: Change value of R317 to 560K

09/19/01 Written by Jerry

P29: Change value of PR29 to 174K_1%

P32: Change value of PR123 to 52.3K_1%

***** Rev2.0 PIR List *****

10/03/01 Written by Jerry

P02: Change value of R27 to 180

 Delete R395

 R395's Pin 1 tie to RP90's pin5

P03: Change value of R353 to 56.2 1%

P28: Add R408 between +3V and pin 87 of U3

10/08/01 Written by Jerry

P11, P13, P28: Change footprint of Y1, Y2, and Y3

P23: Change footprint of JP11

P28: Change footprint of JP12

P31: Change footprint of PL5

P26: Change footprint of SW1

10/17/01 Written by Jerry

P02: Add @ at RP25, RP24, RP28, RP27, RP31, RP30, RP 42, RP41,

RP40, RP43, RP44, RP45, RP46, RP47, RP48, RP49, RP7, RP1, RP6,

RP14, RP20, RP19, RP22, RP9, RP10, RP11, RP8, RP2, and R7

P06: Remove @ at R104

P11: Add @ at R176

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